

ABSTRACT OF THE DISCLOSURE

There is disclosed a video apparatus, comprising:
a serial receiver for receiving, reproducing and outputting
5 a serially transmitted synchronous clock, a control signal,
and a signal containing video data; and a phase locked loop
(PLL) for generating a differential clock having a phase
different from that of the signal input from the serial
receiver, and a main clock having a phase different from
10 that of the signal input from the serial receiver, and a
frequency twice as much as that of the differential clock,
based on the signal input from the serial receiver. It is
possible to set optimal timing for a clock signal and a
video data signal to the component of the latter part of
15 the serial receiver. Moreover, it is possible to supply a
clock signal having no drifting caused by a temperature
change to the component of the latter part of the serial
receiver.